

Appl. No. 10/659,076

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AUG 02 2006**REMARKS**

In response to the Office action dated May 2, 2006, Applicants respectfully request reconsideration based on the following remarks. Applicants respectfully submit that the claims as they presently stand are in condition for allowance.

Claims 1-12 are pending in the present Application. Claims 1, 2 and 8 are rejected. Applicants acknowledge that claims 3-7, 11 and 12 are allowed and that claims 9 and 10 are objected to for depending on a rejected base claim, but are otherwise allowable. Applicants gratefully thank the Examiner for indication of the same.

Reconsideration and allowance of the claims are respectfully requested in view of the following remarks.

Claim Rejections Under 35 U.S.C. § 112**Claims 2 and 8-10**

Claims 2 and 8-10 stand rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. In particular, claims 2 and 8 specify that the "first clock signal is synchronized with a horizontal synchronization signal from an external device." The Examiner alleges that this recitation is in contravention of the disclosure on page 8, lines 3-8 of the specification.

However, Applicants respectfully direct the Examiner's attention to page 10, line 25 to page 11, line 14 of the specification as originally filed for support of claims 2 and 8. In particular, page 10, line 25 to page 11, line 3 specify, *inter alia*, "[t]he first clock generator 601 of the timing controller 600 generates the first clock signal DCCLK, and the second clock generator 602 generates the second clock M, in synchronization with a horizontal synchronization signal Hsync applied from an external image source not shown in the figure. . . . the first clock generator 601 generates the first clock signal DCCLK synchronized with the horizontal synchronization signal Hsync to make the first clock signal DCCLK synchronized with the common voltage in the timing controller 600 of the second embodiment of the present invention. Therefore, the first clock signal DCCLK and the second clock signal M are synchronized with each other."

Page 2 of 4

YOM-056 / OPP021268US

Appl. No. 10/659,076

Accordingly, it is respectfully requested that the rejection to claims 2 and 8, including claims 9 and 10 depending from claim 8, be withdrawn.

Claim Rejections Under 35 U.S.C. § 102(e)

Claim 1

Claim 1 stands rejected under 35 U.S.C. § 102(e) as being anticipated by Senda et al. (U.S. Patent No. 6,950,080). Applicants respectfully traverse.

To anticipate a claim under 35 U.S.C. § 102, a single source must contain all of the elements of the claim. *Lewmar Marine Inc. v. Barlent, Inc.*, 827 F.2d 744, 747, 3 U.S.P.Q.2d 1766, 1768 (Fed. Cir. 1987), *cert. denied*, 484 U.S. 1007 (1988). Moreover, the single source must disclose all of the claimed elements "arranged as in the claim." *Structural Rubber Prods. Co. v. Park Rubber Co.*, 749 F.2d 707, 716, 223 U.S.P.Q. 1264, 1271 (Fed. Cir. 1984).

It is respectfully noted that the Examiner concedes that the limitations of claim 3 are not disclosed in Senda et al. It is also noted that claim 3 has been indicated as being allowed, but reason for allowance are absent. It is further respectfully submitted that the limitations of allowed claim 3 are reflected in claim 1, and therefore is allowable for at least this reason.

More specifically, claim 3 specifies, *inter alia*, "a driving voltage generator for boosting a voltage according to a booster clock signal and for generating the gate voltages and the common voltage based on the boosted voltage, wherein the booster clock signal is synchronized with the common voltage."

Analogously, claim 1 specifies, *inter alia*, "a booster for boosting a voltage according to a first applied clock signal (e.g., booster clock signal) and . . . a common voltage based on the boosted voltage according to a second applied clock signal; and . . . generating gate voltages including a gate-on voltage and a gate-off voltage based on the boosted voltage . . . wherein the first clock signal is synchronized with the common voltage."

Since the same limitations of allowed claim 3 are reflected in claim 1, claim 1 is respectfully submitted as being allowable for defining over Senda et al. for at least this reason.

Furthermore, column 4, lines 43-54 of Senda et al. relied on by the Examiner, do not teach or suggest a booster for boosting a voltage according to a first applied clock signal and outputting the boosted voltage; a common voltage generator for generating a common voltage based on the boosted voltage according to a second applied clock signal; and a gate voltage

Appl. No. 10/659,076

generator for generating gate voltages including a gate-on voltage and a gate-off voltage based on the boosted voltage according to the second clock signal wherein the first clock signal is synchronized with the common voltage", as in claim 1.

Thus, claim 1 defines over Senda et al. for at least this reason as well.

Accordingly, it is respectfully requested that the rejection to claim 1 under § 102(e) b withdrawn.

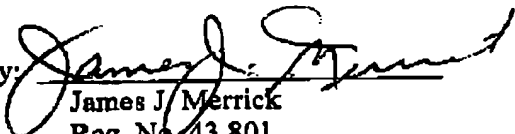
Conclusion

In view of the foregoing, it is respectfully submitted that the instant application is in condition for allowance. Accordingly, it is respectfully requested that this application be allowed and a Notice of Allowance issued. If the Examiner believes that a telephone conference with Applicants' attorneys would be advantageous to the disposition of this case, the Examiner is cordially requested to telephone the undersigned.

In the event the Commissioner of Patents and Trademarks deems additional fees to be due in connection with this application, Applicants' attorney hereby authorizes that such fee be charged to Deposit Account No. 06-1130.

Respectfully submitted,

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